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## UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under

Attorney Docket No.	02-IND-015				
First Inventor	Rajesh NARWAL				
Title	VOLTAGE LEVEL TRANSLATOR FOR TRANSLATING LOW TO HIGH VOLTAGE LEVELS IN DIGITAL INTEGRATED CIRCUITS				
Express Mail Label No.	EL533428405US				

70	37 CFR	R 1.53(b))	Title		TRANSLATOR FOR TRANSLATING LOW TO HIGH VOLTAGE LEVELS IN DIGITAL INTEGRATED CIRCUITS		
Express		Express M	lail Label No. EL53342		EL53342	8405US	
	APPLI	ICATION ELEMENTS	Commissioner for Patents Mail Stop Patent Application Alexandria, VA 22313-1450				
pro	(submit an original and a duplicate for fee processing)  Applicant claims small entity status.  See 37 CFR 1.27  Specification [ total pages 11 ] (preferred Arrangement set forth below)  - Descriptive title of the Invention  - Cross References to Related Applications  - Statement Regarding Fed sponsored R&D  - Reference to sequence listing, a table, or a computer program listing appendix  - Background of the Invention  - Brief Summary of the Invention  - Brief Description of the Drawings  - Detailed Description  - Claim(s)  - Abstract of the Disclosure  4.		6. Application Data Sheet. (See 37 CFR 1.76)  7. CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)  8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)  a. Computer Readable Form  b. Specification Sequence Listing on:  i. CD-ROM or CD-R (2 copies); or  ii. paper  c. Statements verifying identity of above copies  ACCOMPANYING APPLICATION PARTS  9. Assignment Papers (coversheet/document(s))  10. 37 CFR. 3.73(b) Statement Power of (when there is an assignee) Attorney  11. English Translation Document  12. September Pro/SB/08A Copies of IDS Citations  13. Preliminary Amendment  14. Return Receipt Postcard (MPEP 503)  15. Certified Copy of Priority Document(s)  16. Nonpublication Request Under 35 USC 122(b)(2)(B)(i).Applicant must attach form PTO/SB/35  17. Other: Certificate of Mailing  ox, and supply the requisite information below and in a				
par por	t of the disclosure of the action has been inadvertently	/ISIONAL APPS only: The entire dis ecompanying continuation or division y omitted form the submitted application	uon parts.				
19. CORRESPONDENCE ADDRESS   ☐ Correspondence address below  ☐ Correspondence address below							
Na	ame						
A	ddress						
С	ity	State		ZIP			
C	ountry	<del></del>	lephone		Fax	25 425	
	Name (Print/Type	Carol W. Burton	By	Registration No.	Date	35,465 Septem	ber 29, 2003
			1				

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial, No.

Confirmation No.

Inventor:

Rajesh NARWAL

Filed:

September 29, 2003

TC/A.U.

Examiner:

Docket No.

02-IND-015

Customer No.

25235

Title: VOLTAGE LEVEL

TRANSLATOR FOR

TRANSLATING LOW TO

HIGH VOLTAGE LEVELS IN DIGITAL INTEGRATED

**CIRCUITS** 

## CERTIFICATE OF MAILING BY EXPRESS MAIL

Commissioner for Patents Alexandria, VA 22313-1450

Sir:

The undersigned hereby certifies that the following documents:

- 1. Utility Patent Application Transmittal;
- 2. Specification (7 pages); Claims (3 pages); Abstract (1 page); Formal Drawings (7 Sheets):
- 3. Declaration (unsigned);
- 4. IDS and Form PTO/SB/08A:
- 5. Return Card; and

this Certificate of Mailing relating to the above application, were deposited as "Express Mail," Mailing Label No. EL533428405US with the U.S. Postal Service, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 29, 2003.

September 29, 2003

September 29, 2003

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